

**IN THE CLAIMS:**

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered). Please AMEND claims \* and ADD new claims \* in accordance with the following:

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28. (CURRENTLY AMENDED) A discharge cell of a surface discharge type plasma display panel, comprising:

a cavity bounded by a pair of opposing and spaced sidewalls of respective barriers, formed on a first substrate, extending commonly with the pair of sidewalls in a first direction;

an address electrode on the first substrate and extending in the first direction;

a pair of display electrodes formed on a surface of a second substrate, covered by an insulating layer and positioned in opposed relationship with the address electrode, the pair of display electrodes extending in a second direction, ~~transversely to and crossing the barriers and the cavity therebetween,~~ and defining the discharge cell, wherein a width of the cell, in the second direction, is approximately one-third a length thereof, in the first direction; and

a phosphor layer disposed within the cavity on one of the first and second substrates, the phosphor layer having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

29. (PREVIOUSLY ADDED) A discharge cell as recited in claim 28, wherein the phosphor layer is formed on the first substrate, aligned within the cavity, and covers the entire surface of the cavity including sidewalls of the pair of barriers and thereby to constitute a discharge cell of a reflecting type plasma display panel.

30. (PREVIOUSLY ADDED) A discharge cell as recited in claim 28, wherein the pair of display electrodes has a discharge gap of a first width at a central portion of a unit luminescent area and a gap of a second, greater width, at both end portions of the unit luminescent area.

31. (PREVIOUSLY ADDED) A discharge cell as recited in claim 28, wherein a top portion of each barrier is of a dark color.

32. (PREVIOUSLY ADDED) A discharge cell as recited in claim 29, wherein a top

portion of each barrier is of a dark color.

33. (CANCELED) .

34. (PREVIOUSLY ADDED) A discharge cell as recited in claim 28, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction and the pair of spaced barriers, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

35. (CURRENTLY AMENDED) A plasma display panel of a surface discharge type and having an array, of plural columns in the first direction and plural rows in a second direction transverse to the first direction, of plural image elements, each image element comprising a respective set of unit luminescent areas, each set of unit luminescent areas comprising a set of discharge cells, wherein each discharge cell comprises:

a cavity bounded by respective opposing and spaced sidewalls of a pair of barriers formed on a first substrate, the cavity extending commonly with the pair of barriers in a first direction;

an address electrode on the first substrate, extending in the first direction,

a pair of display electrodes formed on a surface of a second substrate covered by an insulating layer and positioned in opposed relationship with the ~~barriers~~ address electrode, the pair of display electrodes extending in a second direction, ~~transversely to and crossing the barriers and the cavity therebetween,~~ and defining the discharge cell, and

a phosphor layer disposed within the cavity on the first substrate; and

each set of discharge cells has respective, first and second combined dimensions in the first and second direction which are substantially the same and comprises a common number of discharge cells in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of the discharge cells being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of discharge cells, aligned in the columns of the array.

36. (CANCELED)

37. (CANCELED) A plasma display panel as recited in claim 35, wherein:  
each set of discharge cells comprises plural cells having plural, respective and different color phosphor layers, each of which layers having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

38. (CANCELED) A plasma display panel as recited in claim 35, wherein:  
the plural cells of each set are of a common width in the second direction.

39. (CANCELED)

40. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 35 wherein,  
in each discharge cell, the phosphor layer covers the respective, opposing sidewalls of the pair of barriers.

41. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 35 wherein,  
in each discharge cell, the phosphor layer is formed on the first substrate, aligned within the cavity, and covers the address electrode and extends to the respective, opposing sidewalls of the pair of barriers, said phosphor layer having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

42. (PREVIOUSLY ADDED) A plasma display panel recited in claim 35, wherein  
each of the pair of display electrodes of each discharge cell comprises a transparent conductor and a respective metal conductor extending therewith in the second direction, and the pair thereof provides a predetermined discharge gap at a central portion of the cell.

43. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 35 wherein,  
in each discharge cell, the phosphor layer is formed within the cavity and extends to the respective, opposing sidewalls of the barriers and a top portion of each of the barriers has a dark color.

44. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 35, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction and the pair of spaced barriers, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

45. (PREVIOUSLY ADDED) A plasma display panel of a surface discharge type and having an array of plural image elements, arranged in plural columns in a first direction and plural rows in a second direction, transverse to the first direction, wherein each image element comprises a respective set of unit luminescent areas:

each unit luminescent area comprises:

a cavity, bounded by respective opposing and spaced sidewalls of barriers formed on a first substrate and extending in the first direction,

an address electrode of the first substrate extending in the first direction,

a pair of display electrodes formed on a second substrate, covered by a dielectric layer and arranged to constitute a corresponding row of the array in opposed relationship with the cavity, and

a phosphor layer disposed on an inside surface of the cavity on the first substrate with a thickness in a range of 10  $\mu\text{m}$ -50  $\mu\text{m}$ ; and

each set of unit luminescent areas comprises a common number of unit luminescent areas in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of unit luminescent areas being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of unit luminescent areas, aligned in the columns of the array.

46. (CURRENTLY ADDED) A discharge cell of a surface discharge type plasma display panel, comprising:

a cavity bounded by respective opposing and spaced sidewalls of a pair of barriers superposed on a first substrate, the cavity extending commonly with the pair of barriers in a first direction;

an address electrode superposed on the first substrate, adjacent a bottom of the cavity

and extending in the first direction;

a pair of display electrodes superposed on a surface of a second substrate, covered by an insulating layer and positioned in opposed relationship with respect to the address electrode, the pair of display electrodes extending in a second direction, ~~transversely to and crossing the barriers and the cavity therebetween,~~ and defining the discharge cell, wherein a width of each cell, in the second direction, is approximately one-third a length thereof, in the first direction;  
and

a phosphor layer disposed within the cavity and superposed on one of the first and second substrates, the phosphor layer having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

47. (PREVIOUSLY ADDED) A discharge cell as recited in claim 46, wherein the phosphor layer is superposed on and covers the address electrode and exposed portions of the first substrate between the spaced and opposing sidewalls and substantially the entire respective surfaces of the spaced and opposing sidewalls of the pair of barriers.

48. (PREVIOUSLY ADDED) A discharge cell as recited in claim 46, wherein the pair of display electrodes has a discharge gap of a first width at a central portion of a discharge cell and a gap of a second, greater width, at both end portions of the discharge cell.

49. (PREVIOUSLY ADDED) A discharge cell as recited in claim 46, wherein a top portion of each barrier is of a dark color.

50. (PREVIOUSLY ADDED) A discharge cell as recited in claim 47, wherein a top portion of each barrier is of a dark color.

51. (CANCELED)

52. (PREVIOUSLY ADDED) A discharge cell as recited in claim 46, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction and the pair of spaced barriers, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

53. (CURRENTLY AMENDED) A plasma display panel of a surface discharge type and having an array, of plural columns in the first direction and plural rows in a second direction transverse to the first direction, of plural image elements, each image element comprising a respective set of unit luminescent areas, each set of unit luminescent areas comprising a set of discharge cells, wherein each discharge cell comprises:

a cavity bounded by respective opposing and spaced sidewalls of a pair of parallel barriers superposed on a first substrate, the cavity extending commonly with the pair of barriers in a first direction;

an address electrode superposed on the first substrate, adjacent a bottom of the cavity and extending in the first direction,

a pair of display electrodes superposed on a second substrate covered by an insulating layer and positioned in opposed relationship with respect to the address electrode, the pair of display electrodes extending in a second direction, transversely to and crossing the pair of barriers and the cavity therebetween, and defining the discharge cell, and

a phosphor layer disposed within the cavity and superposed on and covering the address electrode ~~and the opposed and spaced sidewalls~~; and

each set of discharge cells has respective, first and second combined dimensions in the first and second directions which are substantially the same and comprises a common number of discharge cells in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of the discharge cells being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of discharge cells, aligned in the columns of the array.

54. (CANCELED)

55. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 53, wherein: each set of discharge cells comprises plural cells having plural, respective and different color phosphor layers, each of which layers having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

56. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 53, wherein: the plural cells of each set are of a common width in the second direction.

57. (CURRENTLY AMENDED) A plasma display panel as recited in claim 53 wherein, in each discharge cell, the phosphor layer, further, covers the respective, opposing sidewalls of the pair of barriers.

58. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 53, wherein said phosphor layer has a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

59. (PREVIOUSLY ADDED) A plasma display panel recited in claim 53, wherein each of the pair of display electrodes of each discharge cell comprises a transparent conductor and a respective metal conductor extending therewith in the second direction, and the pair thereof provides a predetermined discharge gap at a central portion of the cell.

60. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 53 wherein, in each discharge cell, the phosphor layer is formed within the cavity and extends to the respective, opposing sidewalls of the barriers and a top portion of each of the barriers has a dark color.

61. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 53, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction and the pair of spaced barriers, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

62. (PREVIOUSLY ADDED) A plasma display panel of a surface discharge type and having first and second substrates and an array of plural image elements therebetween, arranged in plural columns in a first direction and plural rows in a second direction, transverse to the first direction, wherein:

each image element comprises a respective set of unit luminescent areas and each unit luminescent area comprises:

a cavity, bounded by respective opposing and spaced sidewalls of a pair of spaced barriers superposed on the first substrate and extending in the first direction,

an address electrode superposed on the first substrate, adjacent a bottom of the cavity and extending in the first direction,



a pair of display electrodes superposed on the second substrate, covered by a dielectric layer and arranged to constitute a corresponding row of the array in opposed relationship with the cavity, and

a phosphor layer within the cavity of a thickness in a range of 10  $\mu\text{m}$ -50  $\mu\text{m}$ ; and each set of unit luminescent areas comprises a common number of unit luminescent areas in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of unit luminescent areas being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of unit luminescent areas, aligned in the columns of the array.

63. (CURRENTLY AMENDED) A discharge cell of a surface discharge type plasma display panel, comprising:

a cavity bounded at least in part by a respective cavity sidewall supported by a back substrate;

an address electrode supported by the ~~first~~ back substrate, aligned with the cavity and extending in a first direction;

a pair of display electrodes supported by a front substrate, covered by an insulating layer and positioned in opposed, spaced relationship with respect to a portion of the aligned address electrode and defining the discharge cell therebetween, said pair of display electrodes ~~traversing the cavity sidewalls and~~ extending in a second direction; and

a phosphor layer disposed within the cavity and supported on the cavity sidewall and the portion of the aligned address electrode a width of each disclosed cell, in the second direction, is approximately one-third a length thereof, in the first direction.

64. (PREVIOUSLY ADDED) A discharge cell as recited in claim 63, wherein the phosphor layer has a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

65. (PREVIOUSLY ADDED) A discharge cell as recited in claim 64, wherein a top portion of each cavity sidewall is of a dark color.

66. (PREVIOUSLY ADDED) A discharge cell as recited in claim 63, wherein the pair of display electrodes has a discharge gap of a first width at a central portion of a discharge cell and a gap of a second, greater width, at both end portions of the discharge cell.

67. (PREVIOUSLY ADDED) A discharge cell as recited in claim 63, wherein the address electrode is disposed adjacent a bottom of the cavity.

68. (CANCELED)

69. (PREVIOUSLY ADDED) A discharge cell as recited in claim 63, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

70. (CURRENTLY AMENDED) A plasma display panel of a surface discharge type and having an array, of plural columns in the first direction and plural rows in a second direction transverse to the first direction, of plural image elements, each image element comprising a respective set of unit luminescent areas, each set of unit luminescent areas comprising a set of discharge cells, wherein each discharge cell comprises:

a cavity bounded at least in part by a respective cavity sidewall supported by a back substrate;

an address electrode supported by the first back substrate, aligned with the cavity and extending in a first direction;

a pair of display electrodes supported by a front substrate, covered by an insulating layer and positioned in opposed, spaced relationship with respect to, and extending in a second direction and crossing, a portion of the aligned address electrode and defining the discharge cell therebetween;

a phosphor layer disposed within the cavity and supported on the cavity sidewall and the portion of the aligned address electrode; and

each set of discharge cells has respective, first and second combined dimensions in the first and second directions which are substantially the same comprises a common number of discharge cells in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of the discharge cells being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of discharge cells, aligned in the columns of the array.

71. (CANCELED)

72. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70, wherein: each set of discharge cells comprises plural cells having plural, respective and different color phosphor layers, each of which layers having a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

73. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70, wherein: the plural cells of each set are of a common width in the second direction.

74. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70 wherein, in each discharge cell, the phosphor layer covers the respective, opposing sidewalls of the pair of barriers.

75. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70 wherein, in each discharge cell, the phosphor layer covers the address electrode and has a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

76. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70 wherein, in each discharge cell, a top portion of each cavity sidewall has a dark color.

77. (PREVIOUSLY ADDED) A plasma display panel recited in claim 70, wherein each of the pair of display electrodes of each discharge cell comprises a transparent conductor and a respective metal conductor extending therewith in the second direction, and the pair thereof provides a predetermined discharge gap at a central portion of the discharge cell.

78. (PREVIOUSLY ADDED) A plasma display panel as recited in claim 70, wherein each of the pair of display electrodes comprises a metal conductor extending in the second direction, transverse to the first direction, the pair of metal conductors having a combined width in the first direction which is limited so as not to block more than 21% of light emitted from the discharge cell.

79. (CURRENTLY AMENDED) A plasma display panel of a surface discharge type and having front and back substrates and an array of plural image elements therebetween, arranged in plural columns in a first direction and plural rows in a second direction, transverse to the first direction, wherein:

each image element comprises a respective set of unit luminescent areas and each unit luminescent area comprises:

a cavity, bounded by a respective cavity sidewall, supported by the first back substrate, an address electrode supported by the back substrate, aligned with the cavity and extending in the first direction,

a pair of display electrodes supported by the front substrate, covered by a dielectric layer and arranged to constitute a corresponding row of the array in opposed relationship with the cavity, and

a phosphor layer within the cavity, supported by the back substrate, and having a thickness in a range of 10  $\mu\text{m}$ -50  $\mu\text{m}$ ; and

each set of unit luminescent areas comprises a common number of unit luminescent areas in successively spaced adjacent positions in the second direction, the respective phosphor layers of each set of unit luminescent areas being in a common sequence of respective, different colors, and the plural rows of the array having respective, common numbers of sets of unit luminescent areas, aligned in the columns of the array.

80. (CURRENTLY AMENDED) A surface discharge type plasma display panel, comprising:

front and rear substrates in opposing, spaced relationship;

discharge spaces defined by respective sidewall surfaces supported by the rear substrate, extending in parallel spaced relationship in first and second transverse directions on the rear substrate;

respective phosphor layers in the discharge spaces and covering respective bottom and sidewall surfaces of the discharge spaces and supported by the rear substrate; and

address electrodes corresponding to respective phosphor layers and supported on the rear substrate, portions of each address electrode underlying respective corresponding bottoms of the respective phosphor layers covering the bottom surfaces of the discharge spaces being aligned in one of the first and second transverse directions.

81. (PREVIOUSLY ADDED) The plasma display panel as recited in claim 80, further comprising:

a dielectric layer formed on the rear substrate; and  
barriers formed on the dielectric layer defining the discharge spaces.

82. (PREVIOUSLY ADDED) The plasma display panel as recited in claim 80, wherein the phosphor layer has a thickness in a range of from 10  $\mu\text{m}$  to 50  $\mu\text{m}$ .

83. (CURRENTLY AMENDED) The plasma display panel as recited in claim ~~80~~ 81, wherein the address electrodes are formed on the ~~underlying~~ dielectric layer.

84. (PREVIOUSLY ADDED) The plasma display panel as recited in claim 80, further comprising barriers formed on the rear substrate and defining the discharge spaces.